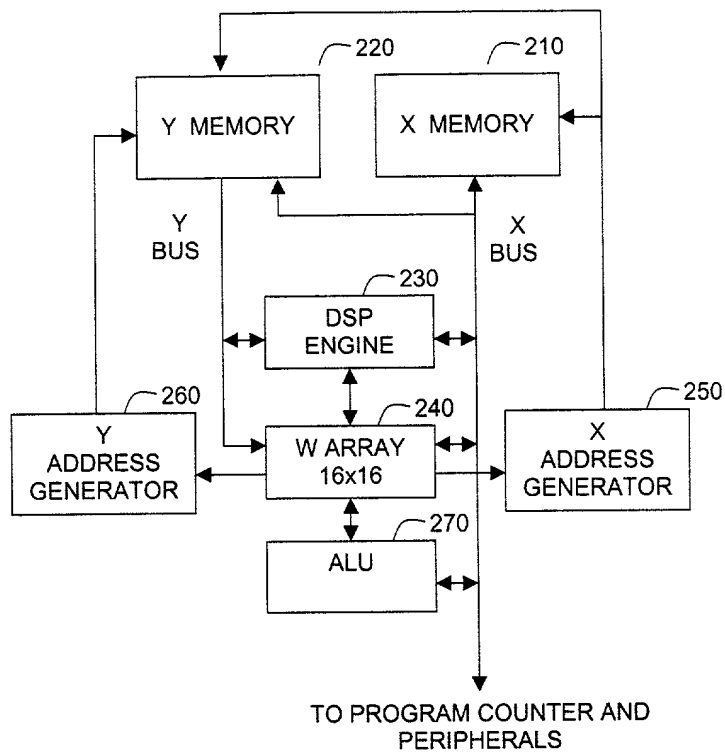
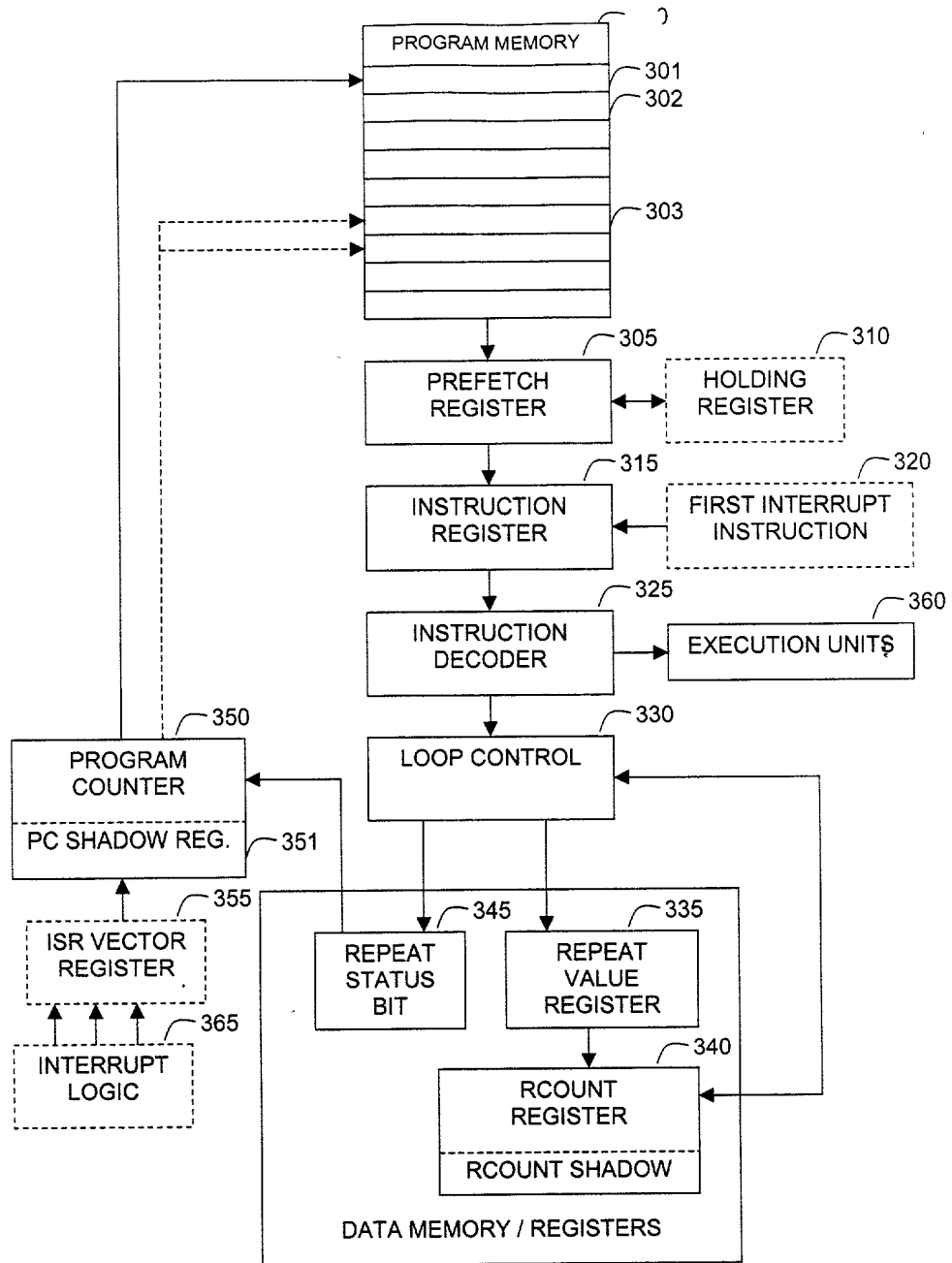


**FIG. 1**

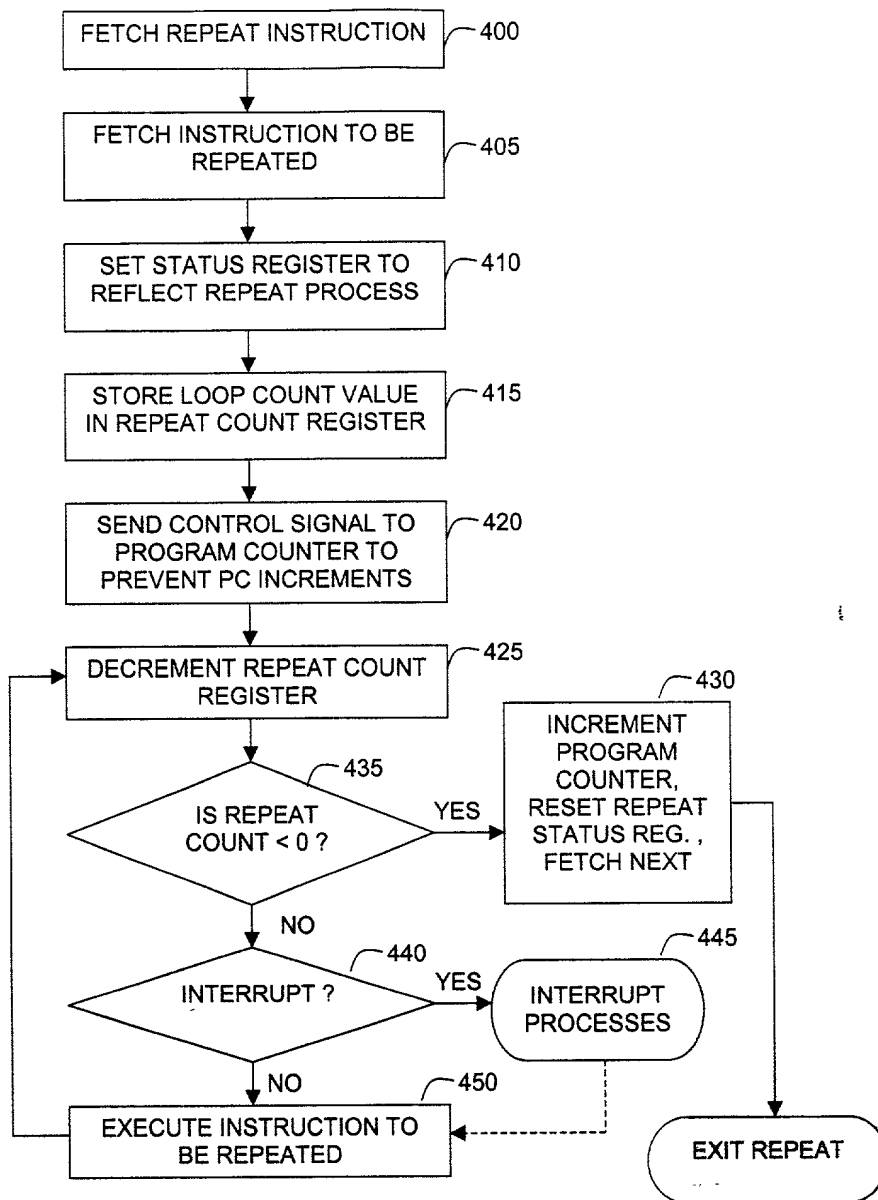
X AND Y DATA BUS IMPLEMENTATION



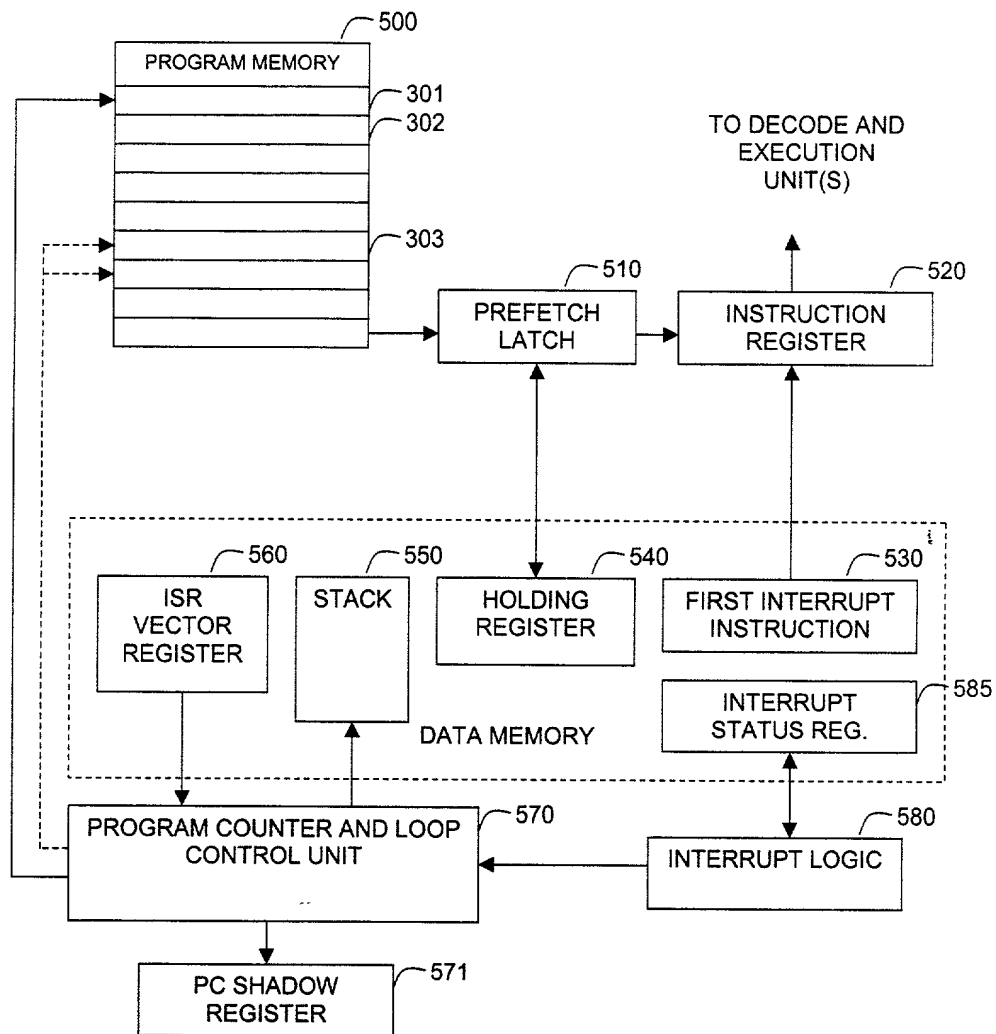
**FIG. 2**



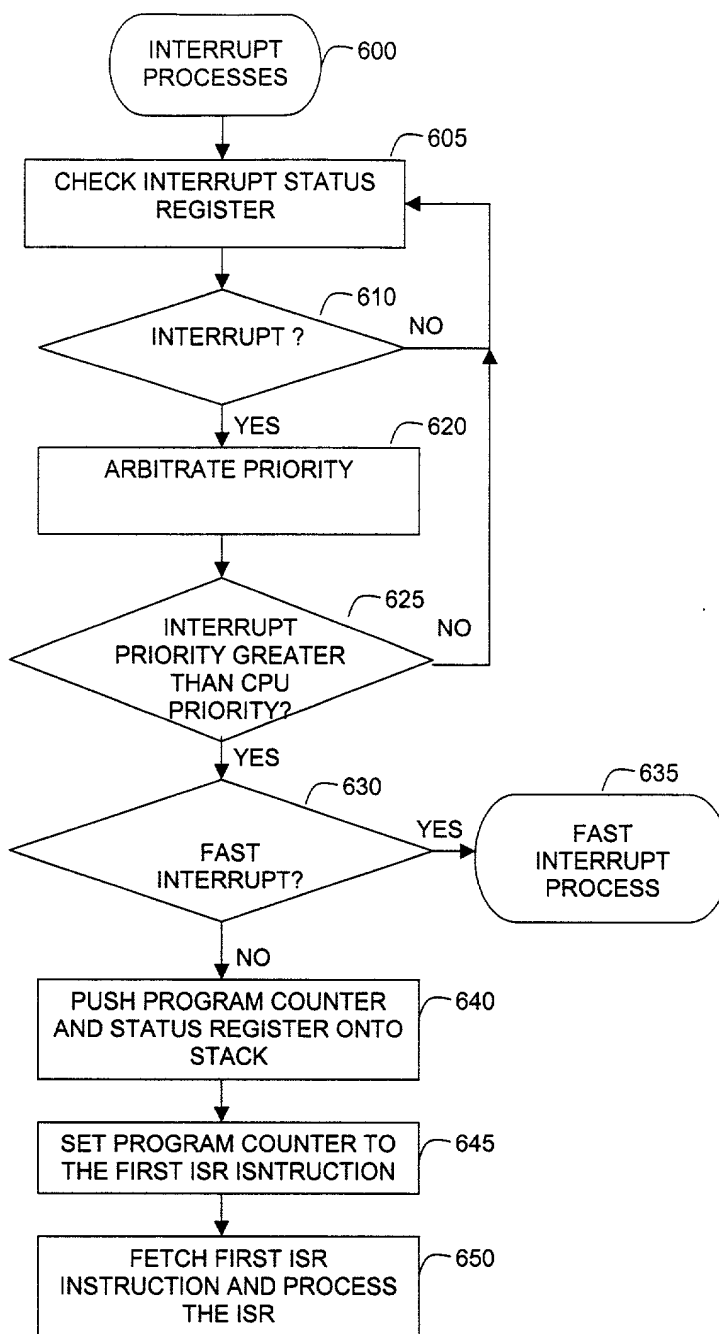
**FIG. 3**



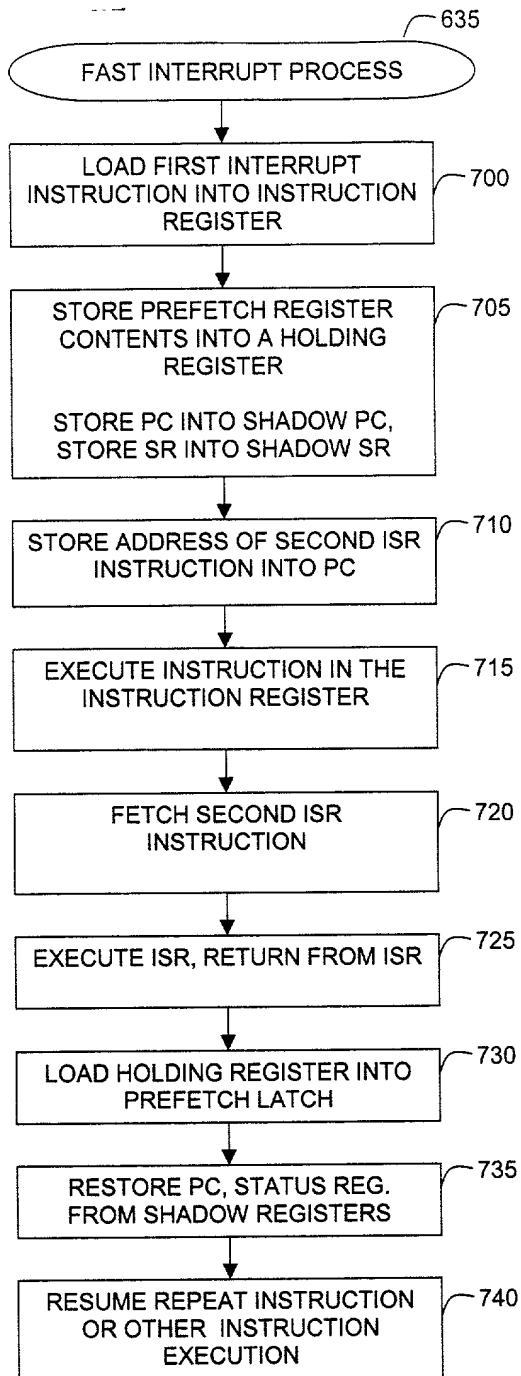
**FIG. 4**



**FIG. 5**



**FIG. 6A**



**FIG. 6B**